

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:)	"Express Mail" Mailing Label No. <u>EV 321709252</u>
Ritsuko KAWASAKI et al)	Date of Deposit <u>August 4, 2003</u>
Serial No.: Not Assigned)	I hereby certify that this correspondence is being
Filed: Herewith)	deposited with the United States Postal Service
Art Unit:)	"Express Mail Post Office to Addressee" service
For: SEMICONDUCTOR DEVICE AND A METHOD)	under 37 CFR1.10 on the date indicated above
OF MANUFACTURING THE SAME)	and is addressed to: Commissioner for Patents,
)	P.O. Box 1450, Alexandria, VA 22313-1450
)	Name: <u>Yue & Ruan</u>
)	(typed or printed)
)	Signature <u>Yue & Ruan</u>

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the references was submitted to the Patent Office or was cited by the Examiner in the pending prior application Serial No. 09/905,587 filed July 13, 2001, or in its parent application, Serial No. 09/532,690, filed March 22, 2000, (now US Patent no. 6,281,552 issued August 28, 2001) which is being relied upon for an earlier filing date under 35 U.S.C. 120 (37 C.F.R. §1.98(d)).

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are

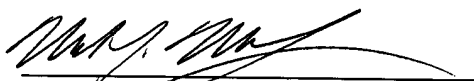
pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has not been received in this case, so applicant does not believe that a fee is due. However, if any such fee is required, please charge our Deposit Account No. 50/1039.

Respectfully submitted,


Mark J. Murphy
Registration No.: 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500

LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Atty. Docket No.</u> 0553-0168.02	<u>Serial No.</u> Not Assigned
	<u>Applicant</u> Ritsuko KAWASAKI et al	
	<u>Filing Date</u> Herewith	<u>Group</u>

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,247,190	09/21/93	Friend et al	257	40	12/28/90
	5,262,655	11/16/93	Ashida	257	66	03/17/92
	5,323,042	06/21/94	Matsumoto	257	350	11/13/92
	5,399,502	03/21/95	Friend et al	437	1	05/05/93
	5,496,756	03/05/96	Sharma et al	437	52	01/30/95
	5,554,548	09/10/96	Sundaresan	437	41	04/24/95
	5,594,569	01/14/97	Konuma et al	349	122	07/20/94
	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
	5,903,014	05/11/99	Ino et al	257	66	11/27/96
	5,923,962	07/13/99	Ohtani et al	438	150	04/28/95
	5,998,841	12/1999	Suzawa	257	350	
	6,046,479	04/2000	Young et al	257	350	
	6,166,397	12/2000	Yamazaki et al	257	350	
	6,166,414	12/2000	Miyazaki et al	257	369	
	6,172,671	01/2001	Shibuya et al	345	205	
	6,180,982	01/2001	Zhang et al	257	347	
	6,281,552 B1	08/28/01	Kawasaki et al	257	350	03/22/00

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	NAME	English Abstract	English Trans.	FILING DATE
	WO 90/13148	11/01/90	Cambridge Research and Innovation Ltd.			04/18/90
	JP 7-130652	05/19/95	Semiconductor Energy Lab Co., Ltd	X		10/29/93
	JP 10-092576	04/10/98	Cambridge Display Technology Ltd.	X		04/18/97

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OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS
(Including Author, Title, Date, Pertinent Pages)

- 1) HATANO, M. et al, "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT with High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.
- 2) SHIMOKAWA, R. et al, "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, vol. 27, no. 5, pp. 751-758, May, 1988.
- 3) YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, 1997.
- 4) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, 1998.
- 5) SCHENK, H. et al, "Polymers for Light Emitting Diodes," EURODISPLAY '99, Proceedings of the 19th International Display Research Conference, Berlin, Germany, September 6-9, 1999, pp. 33-37.

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.